



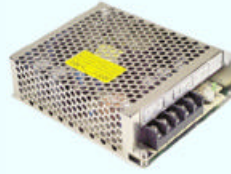
# MEAN WELL

## SWITCHING POWER SUPPLY

ISO-9001 CERTIFIED MANUFACTURER

# S-35 SERIES

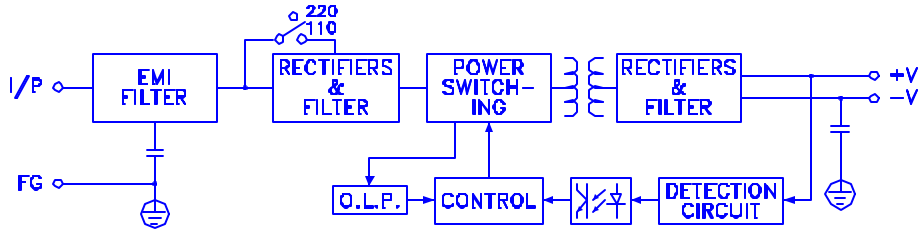
- .LOW COST, HIGH RELIABILITY
- .105°C OUTPUT CAPACITOR
- .INTERNATIONAL AC INPUT RANGE
- .HIGH EFFICIENCY, LOW WORKING TEMPERATURE
- .SOFT-START CIRCUIT, LIMITING AC SURGE CURRENT
- .SHORT CIRCUIT, OVERLOAD PROTECTED
- .COMPACT SIZE, LIGHT WEIGHT
- .100% FULL LOAD BURN-IN TEST
- .BUILT IN EMI FILTER, LOW RIPPLE NOISE



MODEL	S-35-5	S-35-12	S-35-15	S-35-24
SPECIFICATION				
DC OUTPUT VOLTAGE	5V	12V	15V	24V
OUTPUT V. TOLERANCE	±2%	±1%	±1%	±1%
OUTPUT RATED CURRENT	7A	3A	2.4A	1.5A
OUTPUT CURRENT RANGE	0-7A	0-3A	0-2.4A	0-1.5A
RIPPLE & NOISE	75mVp-p	100mVp-p	100mVp-p	100mVp-p
LINE REGULATION	±0.5%	±0.5%	±0.5%	±0.5%
LOAD REGULATION	±1%	±0.5%	±0.5%	±0.5%
DC OUTPUT POWER	35W	36W	36W	36W
EFFICIENCY	70%	76%	78%	78%
DC VOLTAGE ADJ.	+10, -5%	±10%	±10%	±10%
INPUT VOLTAGE RANGE	85~132VAC / 170~264VAC SELECTED BY SWITCH 47~63Hz; 240~370VDC			
AC CURRENT	0.8A/115V 0.45A/230V			
INRUSH CURRENT	COLD START 18A/115V 36A/230V			
LEAKAGE CURRENT	<0.5mA/240VAC			
OVERLOAD PROTECTION	105%~150% TYPE:FOLDBACK CURRENT LIMITING RESET:AUTO RECOVERY			
OVER VOLTAGE PROTECTION	-----			
OVER TEMP. PROTECTION	-----			
TEMP. COEFFICIENT	±0.03% / °C (0~50°C)			
SETUP, RISE, HOLD UP TIME	200ms, 100ms, 30ms			
VIBRATION	10~500Hz, 2G 10min./1cycle, PERIOD FOR 60min. EACH AXES			
WITHSTAND VOLTAGE	I/P-O/P:1.5KVAC I/P-FG:1.5KVAC O/P-FG:0.5KVAC			
ISOLATION RESISTANCE	I/P-O/P, I/P-FG, O/P-FG:500VDC / 100M Ohms			
WORKING TEMP., HUMIDITY	-10°C~+60°C(REFER TO OUTPUT DERATING CURVE), 20%~90% RH			
STORAGE TEMP., HUMIDITY	-20°C~+85°C, 10%~95% RH			
DIMENSION	129*98*38mm CASE:903			
WEIGHT	0.4Kgs			
SAFETY STANDARDS	MEET UL1012 REQUIREMENT (NOT APPLY FOR THE MARK)			
EMC STANDARDS	MEET FCC PART15 J CONDUCTION CLASS B			

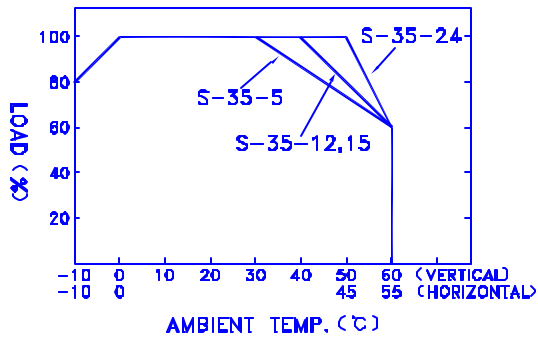
- NOTE :
1. ALL PARAMETERS ARE SPECIFIED AT 230VAC INPUT, RATED LOAD, 25°C 70% RH. AMBIENT.
  2. TOLERANCE INCLUDE SET UP TOLERANCE, LINE REGULATION, LOAD REGULATION.
  3. RIPPLE & NOISE ARE MEASURED AT 20MHz BY USING A 12" TWISTED PAIR TERMINATED WITH A 0.1uF & 47uF CAPACITOR.
  4. LINE REGULATION IS MEASURED FROM LOW LINE TO HIGH LINE AT RATED LOAD.
  5. LOAD REGULATION IS MEASURED FROM 0% TO 100% RATED LOAD.

## BLOCK DIAGRAM

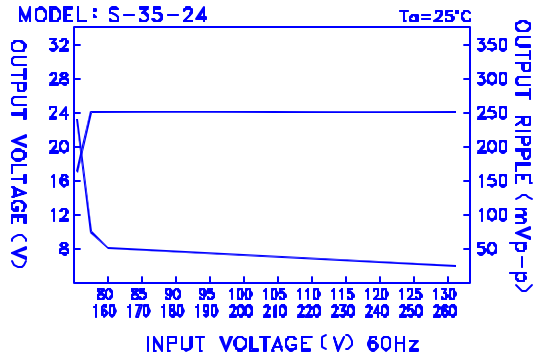


fosc : 37KHz I/P-O/P:RATED

## OUTPUT DERATING



## STATIC CHARACTERISTICS



## DIMENSION (mm)

- ☒ CASE : 903
- ☒ TERMINAL PIN NO. ASSIGNMENT
- ☒ PIN 1,2 : AC INPUT
- ☒ PIN 3 : FG (⊕)
- ☒ PIN 4 : DC OUTPUT -V
- ☒ PIN 5 : DC OUTPUT +V

