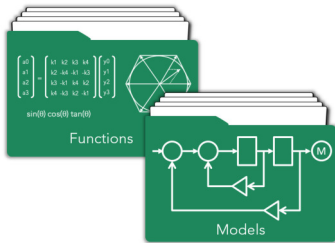


Silicon Mobility

OLEA[®] LIB Inverter Control



On-the-shelf algorithm contents optimized for OLEA[®], boosting application performance with fast time-to-market enablement.

- Cutting edge HEV and EV Inverter control systems
- Configurable and customizable complex algorithms
- x40 computation speed improvement on advanced mathematical and computation functions
- Complete functions design and development in minutes with full integration into OLEA[®] COMPOSER

Build your own control

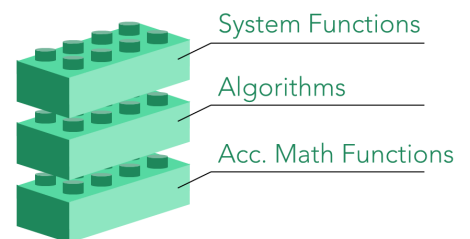
OLEA[®] LIB is a library of advanced software optimized for OLEA[®] and dedicated for the control of Hybrid (HEV) and Electric Vehicles (EV). OLEA[®] LIB is packaged into three levels of integration selectable upon the application needs. Libraries comes as building blocks available as Reference and Target Models Toolbox for MATLAB[®] Simulink, or as HDL pre-defined blocks, and tuned for best used of AMEC[®] FLU. Models out of OLEA[®] LIB are directly usable within OLEA[®] COMPOSER for MiL simulations and automatic code generation.

By using OLEA[®] LIB, developer's reduce drastically the time required to optimize algorithm's porting on OLEA[®] while improving drastically performance, quality and safety of their system.

OLEA[®] LIB System: Efficient System Functions

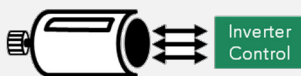
OLEA[®] LIB Algo: Specialized and Enhanced Algorithms

OLEA[®] LIB Math: Accelerated Mathematical Functions



OLEA[®] LIB System

Inverter Control (FoC)



OLEA[®] LIB Algo

- Clarke current transform
- Park current transform
- Decoupling and Flux Weakening
- Inverse Park / Clarke voltage
- Space Vector Modulation PWM
- ID/IQ Regulations
- Motor Speed Regulation
- Tracking loop position estimator
- (Magnetic Resistive, Resolver...)
- Sensorless position estimation (Start-up/
- Low-speed /High speed)
- DC/DC Switching regulation
- AC/DC Switching regulation

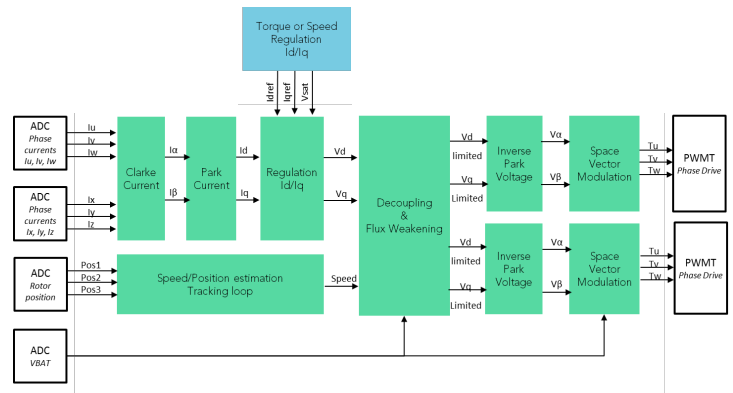
OLEA[®] LIB Math

- COordinate Rotation Dlgital Computer for complex trigonometric operators
- Division operator
- Square Root operator
- Matrix Multiplier
- Proportional Integral Derivate control loop feedback

OLEA[®] LIB System - Inverter Control Features

Complete Inverter Control function optimized for OLEA[®] T222 supporting PMSM or WRSM motors based on Field Oriented Control and Space Vector Modulation algorithms.

- MATLAB / Simulink reference model
- MATLAB / Simulink target model ready for code generation
- Configurable parameters include
 - Variable number of phases
 - Sensor or Sensor-less position estimation
 - Magnetic Resistive, Resolver or Quadrature Sensor Type support
- Diagnostic functions



OLEA[®] LIB Algorithm Features

Speed Regulation	Speed regulation with D/Q-Axis control: PI regulators with Anti Wind-up. DQ-axis Reference current computation	Decoupling & Flux Weakening	DQ-axis Decoupling and flux weakening
Torque Control	Torque control with D/Q-Axis reference current computation	Regulation Id/Iq	IDQ regulation from torque set point
Clarke Current	3 to 2 phases or 6 to 2 phases current transformation	Space Vector Modulation	Space Vector Modulation
Park Current	2 phases current rotation from $\alpha\beta$ to DQ framework	Position Estimation	Position and speed estimation based on Tracking-loop algorithm
Inverse Park Voltage	DQ framework reference voltage transform into $\alpha\beta$ voltage space vector	Sensor-Less Position Estimation	Position and speed estimation: for standstill, low-speed and high-speed operating modes

OLEA[®] LIB Math Features

Operator	Description	Properties	Exec. Cycles	# of Operators
CORDIC (COordinate Rotation DIgital Computer)	<ul style="list-style-type: none"> • $x \cdot \cos(\theta) - y \cdot \sin(\theta)$ • $y \cdot \cos(\theta) + x \cdot \sin(\theta)$ • $\text{atan}(\frac{y}{x})$ • $\sqrt{x^2 + y^2}$ • $x \cdot \cosh(\theta) - y \cdot \sinh(\theta)$ • $y \cdot \cosh(\theta) + x \cdot \sinh(\theta)$ • $\text{atanh}(\frac{y}{x})$ with $\frac{y}{x} \in [-0,8; 0,8]$ • $\sqrt{x^2 - y^2}$ with $\frac{y}{x} \in [-0,8; 0,8]$ 	<ul style="list-style-type: none"> • θ: unsigned 16-bit for angle $\in [0^\circ; 360^\circ]$ • x and y: signed 32-bit fixed point with same fractional bits • atan or atanh: unsigned 16-bit angle $\in [0^\circ; 360^\circ]$ • others: signed 32-bit, fixed point with same fractional bits as inputs 	Resolution in bit + 4	<ul style="list-style-type: none"> • 6 in parallel • Can be pipelined with PID
Division	$A/B = \text{Quotient with remainder}$	<ul style="list-style-type: none"> • A and B: signed or unsigned 24 bits • Quotient: signed or unsigned 24 bits 	26	• 3 in parallel
Square root	<ul style="list-style-type: none"> • \sqrt{R} in unsigned mode • $\sqrt{ R }$ in signed mode 	<ul style="list-style-type: none"> • R (Radical): signed or unsigned 24 bits • Root value: unsigned 24 bits 	2	• 3 in parallel
Matrix Multiplier	<ul style="list-style-type: none"> • $\begin{bmatrix} r_0 \\ r_1 \end{bmatrix} = \begin{bmatrix} a_0 & a_1 & a_2 & a_3 & a_4 & a_5 \\ a_6 & a_7 & a_8 & a_9 & a_{10} & a_{11} \end{bmatrix} \times \begin{bmatrix} b_0 \\ b_1 \\ b_2 \\ b_3 \\ b_4 \\ b_5 \end{bmatrix}$ • $r_0 = \sum_{i=0}^{\text{iter}} (a_i \times b_i \gg Q_f)$ • $r_1 = \sum_{i=0}^{\text{iter}} (a_{i+6} \times b_i \gg Q_f)$ 	<ul style="list-style-type: none"> • a_n: signed 16, 24 or 32 bits • b_n: signed 16, 24 or 32 bits • Q_f: Integer $\in [0; 31]$ • r_n: signed 32 bits • iter: iteration number 	Iter + 4	<ul style="list-style-type: none"> • 3 in parallel • Can be pipelined with a CORDIC
PID (Proportional Integral Derivative controller)	<ul style="list-style-type: none"> ■ Saturation with Anti-windup: ■ Back calculation: if saturation then $\text{integral}_n = K_i \times e_n - K_n (\text{pid}_{n-1} - \text{pid_sat}_{n-1}) + \text{integral}_{n-1}$ ■ Integral clamping: if saturation and $\text{sign}(\text{pid}_{n-1}) = \text{sign}(e_{n-1})$ then $\text{integral}_n = \text{integral}_{n-1}$ 	<ul style="list-style-type: none"> • K_p, K_i, K_d: signed 16, 24 or 32 bits • $P_{\text{int}}, P_{\text{int_ref}}$: signed 16, 24 or 32 bits • $\text{SAT}_{\text{MAX}}, \text{SAT}_{\text{MIN}}, \text{Init}$: signed 32 bits • Q_f: Integer $\in [0; 31]$ 	8	<ul style="list-style-type: none"> • 6 in parallel • Can be pipelined with another PID